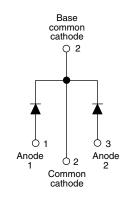


Vishay Semiconductors

## Schottky Rectifier, 2 x 20 A





PRODUCT SUMMARY							
Package	TO-247AC						
I <sub>F(AV)</sub>	2 x 20 A						
$V_R$	15 V						
V <sub>F</sub> at I <sub>F</sub>	See Electrical table						
I <sub>RM</sub> max.	600 mA at 100 °C						
T <sub>J</sub> max.	125 °C						
Diode variation	Common cathode						
E <sub>AS</sub>	10 mJ						

#### **FEATURES**

- 125 °C T<sub>J</sub> operation (V<sub>R</sub> < 5 V)</li>
- Optimized for OR-ing applications
- Ultra low forward voltage drop
- High frequency operation
- Guard ring for enhanced ruggedness and long term reliability
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Designed and qualified according to JEDEC-JESD47
- Material categorization: For definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>





# ROHS COMPLIANT HALOGEN FREE

#### **DESCRIPTION**

The VS-40L15CW... center tap Schottky rectifier module has been optimized for ultra low forward voltage drop specifically for the OR-ing of parallel power supplies. The proprietary barrier technology allows for reliable operation up to 125 °C junction temperature. Typical applications are in parallel switching power supplies, converters, reverse battery protection, and redundant power subsystems.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	CHARACTERISTICS	VALUES	UNITS						
I <sub>F(AV)</sub>	Rectangular waveform	40	A						
V <sub>RRM</sub>		15	V						
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	700	A						
V <sub>F</sub>	19 Apk, T <sub>J</sub> = 125 °C (per leg, typical)	0.25	V						
T <sub>J</sub>		- 55 to 125	°C						

VOLTAGE RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	VS-40L15CWPbF	VS-40L15CW-N3	UNITS				
Maximum DC reverse voltage	V <sub>R</sub>	T <sub>1</sub> = 100 °C	15	15	V				
Maximum working peak reverse voltage	$V_{RWM}$	1J = 100 C	15	15	\ \ \				

ABSOLUTE MAXIMUM RATINGS									
PARAMETER		SYMBOL	TEST COND	VALUES	UNITS				
Maximum average per leg forward current See fig. 5 per device			50 % duty cycle at T <sub>C</sub> = 86 °C, rectangular waveform		20				
		I <sub>F(AV)</sub>	50 % duty cycle at 16 = 00 °C,	40	Α				
Maximum peak one cycle	,		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	700	A			
non-repetitive surge current per leg See fig. 7		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse	rated V <sub>RRM</sub> applied	330				
Non-repetitive avalanche energy per leg		E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 2 A, L = 5 mH		10	mJ			
Repetitive avalanche current per leg I <sub>A</sub>		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s  Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		2	Α			



# **VS-40L15CWPbF, VS-40L15CW-N3**

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ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CO	NDITIONS	TYP.	MAX.	UNITS			
Maximum forward voltage drop per leg See fig. 1		19 A	T <sub>.1</sub> = 25 °C	ı	0.41	V			
	V (1)	40 A	1J=25 C	1	0.52				
	V <sub>FM</sub> <sup>(1)</sup>	19 A	T <sub>.1</sub> = 125 °C	0.25	0.33				
		40 A	1j = 125 C	0.37	0.50				
Reverse leakage current per leg	I <sub>RM</sub> <sup>(1)</sup>	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	-	10	mA			
See fig. 2		T <sub>J</sub> = 100 °C	v <sub>R</sub> = nateu v <sub>R</sub>	-	600	IIIA			
Threshold voltage	V <sub>F(TO)</sub>	T T massimasum				V			
Forward slope resistance	r <sub>t</sub>	$T_J = T_J$ maximum		7.	.6	mΩ			
Maximum junction capacitance per leg	C <sub>T</sub>	V <sub>R</sub> = 5 V <sub>DC</sub> (test signal ran	-	2000	pF				
Typical series inductance per leg	L <sub>S</sub>	Measured lead to lead 5 n	8	-	nH				
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub>	Rated V <sub>R</sub> 10 000						

#### Note

 $^{(1)}\,$  Pulse width < 300  $\mu s,$  duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		YMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction temperature range		TJ		- 55 to 125	°C				
Maximum storage temperature range		T <sub>Stg</sub>		- 55 to 150					
Maximum thermal resistance, junction to case per leg		Б	DC operation See fig. 4	1.4					
Maximum thermal resistance, junction to case per package		- R <sub>thJC</sub>	DC operation	0.7	°C/W				
Typical thermal resistance, case to heatsink	1	R <sub>thCS</sub>	Mounting surface, smooth and greased	0.24					
Approximate weight				6	g				
Approximate weight				0.21	OZ.				
	ninimum		Non-lubricated threads	6 (5)	kgf · cm				
Mounting torque m	aximum		Non-iublicated tilleads	12 (10)	(lbf $\cdot$ in)				
Marking device			Case style TO-247AC (JEDEC)	40L1	5CW				

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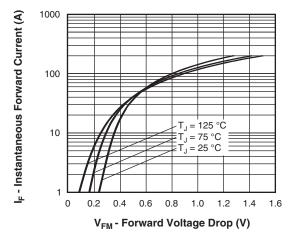


Fig. 1 - Maximum Forward Voltage Drop Characteristics

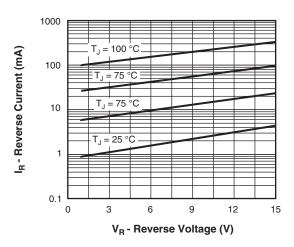


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

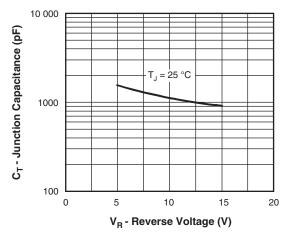


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

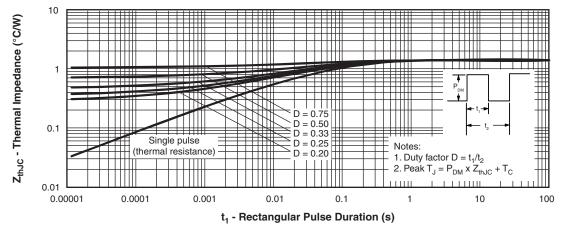


Fig. 4 - Maximum Thermal Impedance Z<sub>thJC</sub> Characteristics

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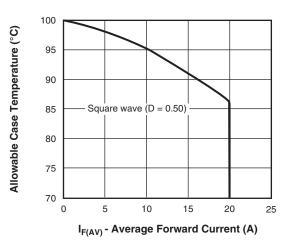


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current

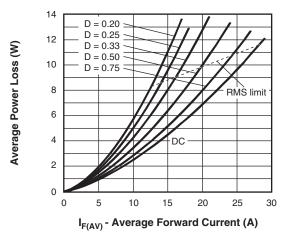


Fig. 6 - Forward Power Loss Characteristics

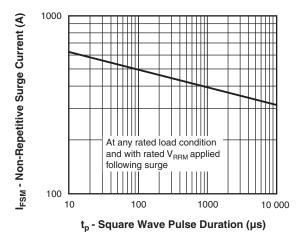


Fig. 7 - Maximum Non-Repetitive Surge Current

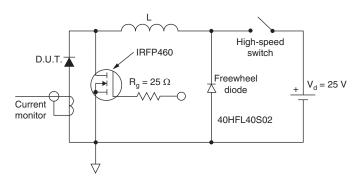


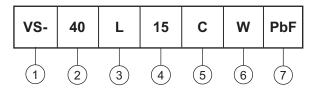
Fig. 8 - Unclamped Inductive Test Circuit

## VS-40L15CWPbF, VS-40L15CW-N3

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#### **ORDERING INFORMATION TABLE**

**Device code** 



- Vishay Semiconductors product

2 - Current rating (40 = 40 A)

3 - Schottky "L" series

- Voltage code (15 = 15 V)

5 - Circuit configuration:

C = Common cathode

6 - Package:

W = TO-247

7 - Environmental digit

• PbF = Lead (Pb)-free and RoHS compliant

• -N3 = Halogen-free, RoHS compliant, and totally lead (Pb)-free

ORDERING INFORMATION (Example)									
PREFERRED P/N QUANTITY PER T/R MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION									
VS-40L15CWPbF	25	500	Antistatic plastic tube						
VS-40L15CW-N3	25	500	Antistatic plastic tube						

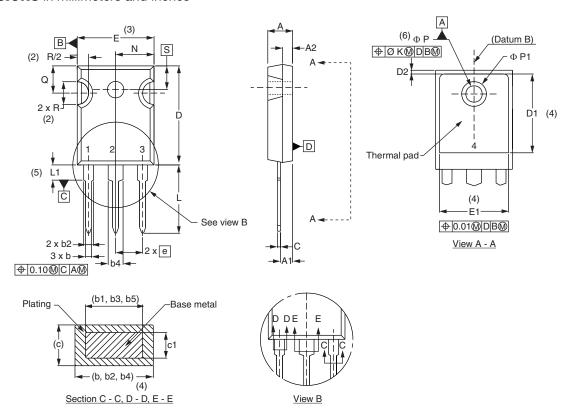
LINKS TO RELATED DOCUMENTS							
Dimensions <u>www.vishay.com/doc?95542</u>							
Dark manulina information	TO-247AC PbF	www.vishay.com/doc?95226					
Part marking information	TO-247AC -N3	www.vishay.com/doc?95007					



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### TO-247 - 50 mils L/F

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIMETERS		MILLIMETERS INCHES		NOTES	SYMBOL	MILLIN	IETERS	INC	HES	NOTES	
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES	STWIBOL	STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209			D2	0.51	1.35	0.020	0.053	
A1	2.21	2.59	0.087	0.102			E	15.29	15.87	0.602	0.625	3
A2	1.17	1.37	0.046	0.054			E1	13.46	-	0.53	-	
b	0.99	1.40	0.039	0.055			е	5.46	BSC	0.215	BSC	
b1	0.99	1.35	0.039	0.053			ØΚ	0.2	254	0.0	)10	
b2	1.65	2.39	0.065	0.094			L	14.20	16.10	0.559	0.634	
b3	1.65	2.34	0.065	0.092			L1	3.71	4.29	0.146	0.169	
b4	2.59	3.43	0.102	0.135			Ν	7.62	BSC	0	.3	
b5	2.59	3.38	0.102	0.133			ØΡ	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	7.39	-	0.291	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	'BSC	

#### Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- $^{(7)}$  Outline conforms to JEDEC® outline TO-247 with exception of dimension c and Q



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